Neural Network Based MPPT Controller for Solar PV Connected Induction Motor

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ABSTRACT

In this paper, Maximum Power Point Tracking Controller is designed based on Neural Network Controller (NNC). This controller will sense the speed of a single phase induction motor which is fed from solar panel. Maximum power point tracking (MPPT) algorithm are required in all photovoltaic (PV) system and in order to increase the efficiency of the system, Incremental Conductance algorithm which is an effective algorithm is used to extract maximum power from the solar panel which supplies an Induction motor of 1HP. To step up the voltage available from the solar panel, the SEPIC DC–DC converter is used. The main advantage of the converter is having non-inverted output. The converter acts as an interface between PV array and motor load. The entire system is modeled and simulated using MATLAB/Simulink software.

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1. INTRODUCTION

Among all renewable energy sources, solar energy attracts more attention because they provide excellent opportunity to generate electricity. Solar energy is a clean renewable resource with zero emission. Power demand is increasing day by day, so we have to switch to renewable energy sources which are eco-friendly and exist abundant in nature. The maximum power point tracking (MPPT) controller is used to improve the efficiency of the PV system, in which Perturb & Observe (P&O) and Incremental Conductance (INC) are frequently used. The incremental conductance algorithm determines the gradient of the P-V curve [1-2]. This method has overcome the disadvantage of the P&O method to track the peak power under fast varying atmospheric condition. The incremental conductance can determine whether the MPPT has reached the maximum power point (MPP) and stop perturbing the operating point or else the relationship between dI/dV & -I/V can be used to determine the direction in which the MPPT operating point must be perturbed [3-6].

A dc to dc converter is needed to boost the voltage from PV panel and commonly available converters are the boost, buck, buck-boost, Cuk, SEPIC [7-8]. In which the single-ended primary inductance converter (SEPIC) is a DC/DC-converter that provides a positively regulated output and non-inverted output. Buck-boost converters are cheaper because they require only a single inductor and a capacitor. But the drawback is the high amount of input current ripple which create harmonics, in many applications, these harmonics require using a large capacitor or an LC filter. This often makes the buck-boost inefficient or expensive, and that can complicate the usage of buck-boost converters is the fact that they invert the output voltage. Cuk converters solve both of these problems by using an extra inductor and capacitor. However,
both buck-boost and Cuk converter operation cause large amounts of electrical stress on the components, this can result in device overheating or failure. SEPIC converters solve both of these problems.

In this paper, SEPIC converter regulates the dc voltage obtained from the solar panel and feeds the single phase inverter. This single phase inverter runs the single phase Induction motor of 1HP capacity [9-10]. The speed of the induction motor is used as a feedback signal from which voltage is derived and error and change in error are obtained and given to Neural Network controller [11]. The generated pulses from the controller are combined with the pulses obtained from the Incremental Conductance Algorithm of a Solar panel and given to SEPIC converter and desired output voltage is produced.

2. PROPOSED SYSTEM

2.1. Solar panel

Solar panel uses the light energy photon from the sun to generate electricity through the photovoltaic (PV) effect. The majority of modules use wafer based cells or thin-film cells based on non-magnetic conductive transition metal or silicon. It can provide nearly permanent power at low operating cost and is virtually free pollution. A typical PV cell produces less than 3 watts at approximately 0.5V DC. A PV module consists of several PV cells connected in series or parallel. Series connections are responsible for increasing the voltage and the parallel connections are responsible for increasing the current. Figure 1 shows the block diagram of the proposed system.

![Block diagram of the proposed scheme](image)

2.1.1. Design of solar panel

To generate 1kW power from the solar panel, four 250W panels is connected in series. Due to the series connection, the voltage is increased and the current remains constant. In Table 1 is solar panel.

<table>
<thead>
<tr>
<th>S.No</th>
<th>SPECIFICATION</th>
<th>RANGE</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>Number of cells</td>
<td>72</td>
</tr>
<tr>
<td>2</td>
<td>Open circuit voltage (V_{oc})</td>
<td>44 V</td>
</tr>
<tr>
<td>3</td>
<td>Short circuit current (I_{sc})</td>
<td>7.58 A</td>
</tr>
<tr>
<td>4</td>
<td>Voltage at maximum power (V_{mp})</td>
<td>35.55 V</td>
</tr>
<tr>
<td>5</td>
<td>Current at maximum power (I_{mp})</td>
<td>7.04 A</td>
</tr>
</tbody>
</table>

2.2. Maximum power point tracking

In wind turbines and PV solar systems, to maximum power extraction is possible under all conditions, if MPPT technique is used. PV solar system has many different configurations. A typical solar panel converts only 30-40(%) of the incident solar irradiation into electrical energy. MPPT increases the efficiency of the solar panel rapidly. If they operate at their MPP despite the inevitable changes in the environment, maximum power from the solar panel can be harvested. Over the past decades, many methods to find the MPP have been published and developed. In that most suitable techniques for medium and, large-size photovoltaic applications are P&O and INC. These techniques have the merits of an easy implementation.
2.2.1. Incremental conductance algorithm

A wide range of MPPT Algorithms are available. Of all the available algorithms, Incremental Conductance Algorithm lends itself well. The incremental Conductance method is often considered, due to its high performances such as easy implementation, high tracking speed, better efficiency and it gets easily adaptable for the changing environmental conditions thus increase the efficiency of PV system. It is found to be the best technique and easily adaptable to the changing environmental conditions. On comparing the efficiency results obtained from Perturb & Observe (P&O) 95% and the Incremental Conductance Algorithm 98%.

This algorithm senses the output current and voltage of the PV array using sensors. The demerits of P&O method to track the peak power under the fast varying atmospheric condition is overcome by INC method. The incremental conductance can determine the MPPT and if the MPP is reached it stops perturbing the operating point.

\[
\frac{dP}{dV}_{MPP} = 0, \\
\frac{d(VI)}{dV} = 0, \\
I\frac{d(V)}{dV} + V\frac{d(I)}{dV} = 0 \\
I + V\frac{d(I)}{dV}_{MPP} = 0 \\
\frac{d(V)}{dV}_{MPP} = -\frac{I}{V}
\]

The term \(-\frac{I}{V}\) represents the instantaneous conductance of the PV panel and the term \(\frac{d(I)}{dV}\) represents incremental conductance of the PV module. This method is based on the fact that the slope of the power curve is zero at the MPP, if the slope is decreasing, MPP lies on the right side and if the slope is increasing, MPP lies on the left side. This can be given by,

\[
\frac{d(I)}{dV}_{MPP} = -\frac{I}{V}, \text{ at the MPP} \\
\frac{d(I)}{dV}_{MPP} > -\frac{I}{V}, \text{ on the left} \\
\frac{d(I)}{dV}_{MPP} < -\frac{I}{V}, \text{ on the right}
\]

The perturbation is repeated until the MPP is reached. Until a change in current is measured, the MPPT continues to operate at the same point. In Figure 2 is flow chart InC algorithm and Basic concept of INC on a P-V curve as shown in Figure 3.

![Flow chart for INC algorithm](image)

**Figure 2. Flow chart for INC algorithm**

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2.3. Sepic converter

The circuit diagram of the SEPIC converter is shown in Figure 4. A SEPIC (single-ended primary inductor converter) is one type of DC-DC converter. It consists of boost converter followed by a buck-boost converter. The main advantage of this converter is capable of providing a non-inverted output (i.e. the output has the same polarity as the input). Its output voltage must be greater than or less than or equal to the input voltage and is widely used in battery operated applications.

The output voltage is controlled by adjusting the duty cycle of the control switch. The control switch is typically a MOSFET, which offers much higher input impedance, low voltage drop and lower switching losses. A SEPIC converter is a fourth order converter, it means these converters have four energy storage elements they are two inductors and two capacitors, and it is used to transfer the energy from input side to output side. The input inductor L1 is together with the MOSFET control switch to be like a boost topology, where the inductor L2 location is similar to a buck-boost topology.

![Power circuit of SEPIC converter](image)

**Figure 4. Power circuit of SEPIC converter**

### 2.3.1 Design calculation

\[
\begin{align*}
V_{IN} &= 142.2V \\
I_{IN} &= 7.04A \\
f &= 15KHZ \\
V_{OUT} &= 230V \\
I_{OUT} &= 4A \\
\end{align*}
\]

Where,

- **V_{IN}** = INPUT VOLTAGE
- **I_{IN}** = INPUT CURRENT
- **f** = FREQUENCY
- **V_{OUT}** = OUTPUT VOLTAGE
\[ V_{IN} = \text{INPUT VOLTAGE} \]

**CALCULATION DUTY CYCLE:**

We know that, \[ D = \frac{T_{ON}}{T} \]

Where,
\[ T = T_{ON} + T_{OFF} \]

Similarly, \[ D = \frac{V_{OUT}}{V_{OUT} + V_{IN}} \]

\[ D = \frac{230}{230 + 142.2} \]

\[ D = 0.6179 \]

**INDUCTOR CALCULATION:**

For Inductor \( L_1 \),
\[ L_1 = \frac{V_{IN} \times D}{2 \times I_{IN} \times f} \]

\[ L_1 = 142.2 \times 0.6179 / 2 \times 7.04 \times 15 \times e^3 \]

\[ L_1 = 0.416e-3 \text{H} \]

For Inductor \( L_2 \),
\[ L_2 = \frac{V_{OUT} \times (1-D)}{2 \times I_{OUT} \times f} \]

\[ L_2 = 230 \times (1-0.6179) / 2 \times 4.5 \times 15 \times e^3 \]

\[ L_2 = 0.6457e-3 \text{H} \]

**CAPACITOR CALCULATION:**

For Capacitor \( C_1 \),
\[ C_1 = \frac{I_{OUT}}{V \times f} \]

Where,
\[ \Delta V = V_{OUT} - V_{IN} \]

\[ C_1 = 3.447e-6 \text{F} \]

For Capacitor \( C_2 \),
\[ C_2 = \frac{I_{OUT}}{V_{OUT} \times f} \]

\[ C_2 = 4.54 / 230 \times 15 \times e^3 \]

\[ C_2 = 1.31e-6 \text{F} \]

2.4. **Neural networks**

A neural network is a mathematical model inspired by biological neural networks as like in a brain. Human brain learns from experience and stores information's as patterns. In some situation, these patterns are very complicated and allow us the ability to recognize individual faces. So the process of storing information as patterns has a new field in computing. This computing use as massive parallel networks and training of this netw done to solve a specific problem [11].

A neural network consists of an interconnected set of artificial neurons, and it process information using a connectionist approach to computation. Integrated circuits are two-dimensional devices with less number of layers for interconnections. In reality, artificial neural networks can be implemented in silicon and basically, all artificial neural networks have a similar structure. In that structure, some neurons interfere to receive input, other neurons interfere with the network output and rest of the neuron has a role but will not be displayed. Basically, the neural network has three layers namely input layer, the output layer and hidden layer. The hidden layer may or may not be present depending on the application used. This neural network for speed control of induction motor has two layers namely input layer and output layer. In Figure 5 is the layout of artificial neural network.

**Figure 5. Layout of artificial neural network**

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2.4.1. Training and artificial neural network

If a network has been framed for a particular application then the next step is training the artificial neural network. To begin this process initial weights are assumed randomly. Training of artificial neural network can be approached in two ways namely supervised and unsupervised. In supervised training, the output is known so input is given as per the desired output. This process involves the comparison of resulting output and desired output so that error value would be generated. The error value so obtained is propagating back and weights are adjusted and above said process continue until desired output is obtained. The set of data which enable this whole process is called “training set”. On other unsupervised training will be preceded with random input as desired output is not predictable. So this process is also called as self-organization [12]. In Figure 6 is layout of Typical Neural Network.

![Typical Neural Network](image)

**Figure 6. Layout of typical neural network**

2.4.2. Components of artificial neural network

There are seven major components involved in the artificial neural network. These components are valid whether the neuron is used for input, output or in the hidden layer [13-14].

a. Determination of weight

A neuron receives much input simultaneously. Each input is provided with corresponding weights so that provides an impact on the processing element. Priority to the inputs depends on the weight. So weight can be described as adaptive coefficients which determine the intensity of the input signal and measure the input connection strength. These strengths can be modified during training set.

b. Summation function

The first process is to compute the weighted sum of all the inputs mathematically input and weights are in vector form and dot products are carried out between them. Geometrically the dot products represent similarity between inputs and weights. The resultant of the vector products depended upon the direction of vector points. The combination of neural inputs depends upon network architecture sometime activation function is also done and its purpose is to allow the output obtained to vary with respect to time.

c. Transfer function

The obtained weighted sum is transformed to working output through the transfer function. During the process, the weighted sum is compared with a threshold value to determine the network output. If the condition is satisfied, then it generates signal otherwise, it won’t produce any signal. The transfer function is generally a non-linear system because linear functions are not useful. Sometimes ramping functions, sigmoid or S-shaped curve are used as transfer functions.

d. Scaling and limiting

The results so obtained are processed through scaling factors and sometimes offset value is added. Limiting is the mechanism which is done when it exceeds the boundary conditions.

e. Output function

Like the biological neuron, there are many inputs and only one output. The output is directly proportional to transfer function result. Competition can occur at one or both of two levels. First, the activity of the artificial neuron is determined and second competition determines the participation of processing element.

f. Error function and back propagated value

In this process difference between current output and desired output is calculated. The change in error is then transformed to error function to match the network architecture. The error value is propagated into the learning function of another processing element; this error term is called as a current error. The current error propagated back and scaled by the learning function multiply by incoming weight to modify the learning cycle.
g. Learning function:
The purpose of learning is to modify the weight of the inputs in each processing element. The weight is changed to obtain desired output which is called as adaption function.

2.4.3. Steps involved in framing ANN

Step 1: Input to the Artificial Neural Network
Change in voltage or error voltage from the motor is given as input to the artificial neural network.

Step 2: Processing of Input
From multiple inputs, a single input is chosen and assumed minimum value is subtracted and multiplied by the gain which is the ratio of output range to input range and in some situation, additional bias is given to the input. This forms the first layer of an artificial neural network.

Step 3: Output Processing
The output of the layer 1 is fed back as an input to this layer 2 and the output would be a two-dimensional element. This element is made dot products with the weights assumed and then given to the transfer function and the process of step 2 is done in a reverse manner.

3. SIMULATION MODELING

3.1. MPPT modeling using Neural Network Controller (NNC)

Figure 7 shows the modeling circuit of Neural Network Controller method and its subsystem with incremental conductance algorithm. It shows that the 1kW solar panel connected to the SEPIC converter along with single phase induction motor (1HP) as a load. The speed of the single-phase induction motor is used as a feedback signal from which voltage is derived and error and change in error are obtained and given to Neural Network Controller. The generated pulses from the controller are combined with the pulses obtained from the Incremental Conductance Algorithm of a Solar panel and given to SEPIC converter and desired output voltage is produced [15].

![Figure 7. Modeling of circuit for Neural Network Controller (NNC)](image-url)
3.2. Modeling of Neural Network Controller subsystem

Figure 8 shows the modeling diagram of the neural network subsystem. This compares the output voltage value with given reference value. According to those values of error and change in error, the output voltage is generated from the trained neural networks.

4. SIMULATION RESULTS AND DISCUSSIONS

4.1.1. Parameters of PV model

The following Figure 9 shows the simulation result of output voltage and output current of the photovoltaic system. The PV panel has been modelled using MATLAB/Simulink and the output voltage and current waveforms have been observed as 143.8V and 6.9A respectively. These are depicted in Figure 9. The output power from the PV panel is observed to be 1000W which has been shown in Figure 10.
4.1.2. Output current & voltage of SEPIC

The output from the PV panel is fed to the SEPIC converter in order to boost the voltage. The duty cycle of the SEPIC converter is adjusted with the use of neural network controller in order to stabilize the voltage at the output of the converter. The neural network controller is embedded with incremental conductance MPPT algorithm which extracts the maximum power from the PV panel under varying irradiance conditions. The voltage at the output of the SEPIC converter is observed to be 230.6V and current is 4.06A which has been shown in Figure 11.

Figure 11. Output voltage & current of SEPIC converter with NNC

4.1.3. Speed of the Motor

The output from the SEPIC converter is fed to the single-phase bridge inverter which converts the voltage to AC in order to feed it to the single phase induction motor. The output speed of the induction motor is observed. It is found that the speed of the motor gradually increases and attains 1000 rpm and after the instant of t=0.5 seconds, the speed of the motor reaches its steady state value of 1500 rpm. Output parameter values are furnished in Table 2, and in Figure 12 is output speed of the motor with NNC.

Table 2. Observed simulation parameters

<table>
<thead>
<tr>
<th>Output voltage</th>
<th>Output current</th>
<th>Speed of the motor</th>
</tr>
</thead>
<tbody>
<tr>
<td>230.6 V</td>
<td>4.05 A</td>
<td>1500 rpm</td>
</tr>
</tbody>
</table>

Figure 12. Output speed of the motor with NNC
Using NNC, the voltage and current from panel is obtained as 143.8V and 6.98A and output power as 1000W. The obtained voltage and current is boosted to 230.6V and 4.05A using SEPIC converter. The inverted voltage is fed to the induction motor at no-load condition which runs at 1500rpm. Comparing with the results of the system with conventional controllers like P,PI and PID controllers, it is observed that the proposed scheme with neural network controller attains the steady state value of speed faster performs better [16]. Table 3 shows the comparison chart of the results from the conventional controller and the neural network controller.

<table>
<thead>
<tr>
<th>Parameters</th>
<th>Conventional controller</th>
<th>Proposed controller</th>
</tr>
</thead>
<tbody>
<tr>
<td>Steady state speed</td>
<td>1490 rpm</td>
<td>1500 rpm</td>
</tr>
<tr>
<td>Settling time</td>
<td>0.75 seconds</td>
<td>0.5 seconds</td>
</tr>
<tr>
<td>Rising time</td>
<td>0.1</td>
<td>0.05 seconds</td>
</tr>
</tbody>
</table>

5. CONCLUSION

This paper highlights the design of the neural network controller for the speed control of single phase induction motor of the rated capacity of 1HP which is driven by solar energy system. The maximum power from the solar panel is extracted using Incremental Conductance Algorithm. The voltage and current obtained from the solar panel is regulated by SEPIC converter which feeds the single phase inverter and that drives the induction motor. The pulses from the respective controller and Incremental Conductance algorithm based MPPT Controller is combined then applied to the SEPIC converter to boost the voltage obtained from solar panel. The speed of the Induction Motor is given as feedback to the controller. The simulation for the neural network controller has been carried out. From simulation results obtained, it is observed that Neural Network controller shows better performance compared to the conventional controller for the speed control of induction motor.

REFERENCES


**BIOGRAPHY OF AUTHOR**

**T. Shanthi** received her bachelor’s degree from Institute of Road and Transport Technology, Erode, Tamilnadu, India in Electrical and Electronics Engineering during 1999. She received her Master’s degree from National Institute of Technology, Tiruchirappalli, India during 2007. She is pursuing PhD at anna university, Chennai in the area of power electronics applications in renewable energy systems She is currently working as Assistant Professor at the department of Electrical and Electronics Engineering in Kumaraguru College of Technology, Coimbatore, India. She has published number of papers in international scopus indexed journals and IEEE conferences. She has number of citations, h-index and i10 index for her publications. Her area of research includes renewable energy systems, hybrid integration, microgrid, electromagnetic fields, power system analysis, soft computing and optimization techniques.